

LIST OF ART CITED BY APPLICANT

(PTO-1449)

ATTY. DOCKET NO.
YHK-0107

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APPLICANT(S)
Won Tae Kim and Soo Seok SIM

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July 8, 2003

GROUP 2821

U.S. PATENT DOCUMENTS

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U.S. PATENT APPLICATION PUBLICATIONS

	*PATENT APPLN. PUB. NO.	*PUB. DATE	*APPLICANT	CLASS	SUBCLASS	
	2004/0239589 A1	12/02/2004	Nishimura et al.	345	60	07/23/2002

U.S. PATENT APPLICATIONS

	*APPLN. NO.	*FILING DATE	*INVENTOR	CLASS	SUBCLASS	

FOREIGN PATENT DOCUMENTS

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OTHER ART (Including Author, Title, Date, Pertinent Pages, Publisher, Place of Publication, Etc.)			
	*de ZWART et al.; "Wall Voltage Fingerprint Method in Three-Electrode PDP Cell"; December 2001.		
	*KIM et al.; "Influence of Ramped Erase Pulse Slope in Reset Period on the Addressing Characteristics in AC-PDP"; December 2001.		
	*KIM et al.; "The Addressing Characteristics of an Alternating Current Plasma Display Panel Adopting a Ramping Reset Pulse"; August 2001.		
	*LEE et al.; "Improvement of Addressing Time and its Dispersion in AC PDP"; December 2001.		
	*LEE et al.; "Improvement of the Contrast Ratio and Reduction of the Reset Period by Current Controlled Ramp Waveform"; December 2001.		
	*LEE et al.; "Improvement of the Luminance and Contrast Ratio by Current Controlled Ramp Waveform in AC PDP"; December 2001.		
	*LEOU et al.; "Study of Discharge Characteristics of a Color PDP Cell Using Ramp Type Setup Voltage Pulse; June 2001.		
	*OVERSLUIZEN et al.; "Characteristics of High-Xe Concentration 4 inch PDP"; December 2001.		
	*PARK et al.; "A Modified Ramp Waveform to Reduce Rest Period in AC Plasma Display Panel"; May 2002.		
	*PARK et al.; "A New Driving Waveform to Improve Dark Room Contrast in Ratio in AC Plasma Display Panel"; July 2002.		
	*PARK et al.; "Improvement of Addressing Time and its Dispersion in AC Plasma Display Panel"; October 2001.		
	*SAKITA et al.; "Analysis of Cell Operation at Address Period Using Wall Voltage Transfer Function in Three-Electrode Surface-Discharge AC PDPs"; December 2001.		
	*SAKITA et al.; "High-speed Address Driving Waveform Analysis Using Wall Voltage Transfer Function for Three Terminals and VT Close Curve in Three-Electrode Surface-Discharge AC PDPs"; June 2001.		
	*SAKITA et al.; "Ramp Setup Design Technique in Three-Electrode Surface-Discharge AC PDPs"; May 2002.		
	*TAMAKA et al.; "A New Progressive Driving Scheme for a PDP with "CASTLE" Structure"; December 2001.		
	*WEBER; "Color Plasma Displays"; May 2002.		
	*YOON et al.; "Selective Charge-inversion Addressing Method for AC Plasma Display Panel"; April 2002.		
EXAMINER		DATE CONSIDERED	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

\\Fk4\Documents\2017\2017-096\168234.doc

* Please note that this reference is not currently available. However, it will be submitted upon availability.